AMENDMENTS TO THE SPECIFICATION

Please amend paragraph [0009] of the specification as follows:

[0009] FIG. 3 more closely shows the rows and columns 349 of a typical CMOS imager 300. Each column 349 includes multiple rows of pixels 350 (such as the pixel 50 illustrated in FIG. 1). Pixel and reset signals from the pixels sensors 350 in a particular column can be read out to a readout circuit 352 associated with that column. The readout circuit 352 includes sample and hold circuitry for acquiring the pixel and reset signals. Signals stored in the readout circuits 352 are sequentially readout column-by-column onto lines 370, 372 to an output stage 354, which in the illustrated example is common to the entire array of pixels 330. The analog output signals Vout1, Vout2 may then be output, for example, to a differential analog circuit that subtracts the reset and pixel signals and sends them to an analog-to-digital converter (ADC). Alternatively, the reset and pixel signals may each be supplied to the analog-to-digital converter.